

PATENT 37310-000114/US

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants:

John S. CLAPP, III et al.

Serial No.:

09/747,924

Group: 2836

Filed:

December 27, 2000

Examiner: D. Nguyen

For:

CURRENT LIMITER FOR MAGNETO-RESISTIVE CIRCUIT

ELEMENT

AMENDMENT

Assistant Commissioner for Patents Washington, D.C. 20231

February 12, 2003

Sir:

Responsive to the Office Action mailed **August 12, 2002** the following amendments and remarks are respectfully submitted in connection with the above-identified application. A Request for Extension of Time is attached hereto and incorporated by reference, thereby extending the date from November 12, 2002 to February 12, 2003.

IN THE DRAWINGS

Please consider the Drawing Change Authorization Request filed herewith.

IN THE SPECIFICATION

Please amend the specification as follows.

Pages 6-7, line 3

The circuit configuration of Figure 2 involves the addition of a current limiter in the form of a third servo loop 72 including not only existing Q2 transistor 56 and R3 resistor, but also now